

Amendments to the Specification:

Please amend the paragraph beginning at line 6 of page 2 and ending at line 11 of page 2 as follows:

A conventional method to measure a transistor matrix is illustrated in Figure 1. As shown, the methodology provides that a pad group 10 is used (as an example, Figure 1 illustrates a 2 x 10 pad group). In the methodology, test structures structures 12 (i.e., transistors) are placed in empty spaces between the pads 14. Using a shared gate and source, one 2 x 10 pad group can hold up to 9 test structures (i.e., transistors).

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Page 2